

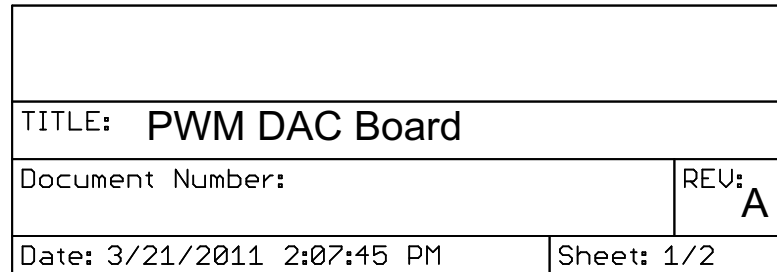
Power Supply and Debug

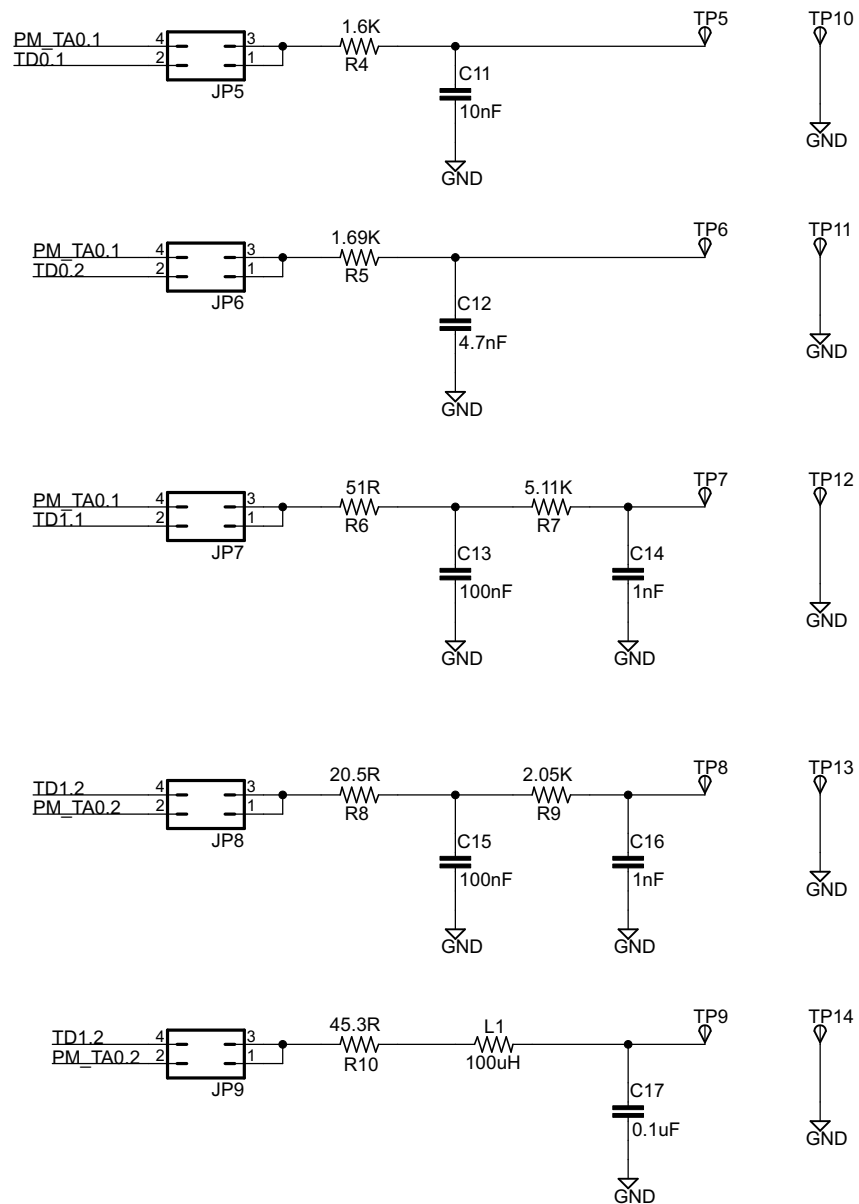
The diagram illustrates the power supply and debug connections for the board. It features a JTAG interface with pins 1 through 14. The connections are as follows:

- JTAG Pins:**
 - Pin 14: TP1
 - Pin 12: TP2
 - Pin 10: TP3
 - Pin 8: EXTPWR
 - Pin 6: GND
 - Pin 4: DVI0_SUPPLY
 - Pin 2: FETPWR
 - Pin 13: TEST
 - Pin 11: GND
 - Pin 9: GND
 - Pin 7: GND
 - Pin 5: RST
 - Pin 3: RST
 - Pin 1: RST
- Power Supply:**
 - VCC is connected to the positive terminal of the power supply.
 - GND is connected to the negative terminal of the power supply.
 - A 47kR resistor (R3) is connected between VCC and GND.
 - A 2.2nF capacitor (C8) is connected between VCC and GND.
- Debug Pins:**
 - JP4 (ext) is connected to EXTPWR.
 - JP4 (int) is connected to FETPWR.
 - JP3 (2) is connected to VCC.
 - JP3 (1) is connected to GND.

Crystal Connections

The diagram shows a crystal circuit. A 32KHz crystal, labeled X1, is connected between the XIN and XOUT pins. A 33pF capacitor, labeled C10, is connected between XIN and GND. Another 33pF capacitor, labeled C9, is connected between XOUT and GND. Both capacitors are marked as DNP (Do Not Populate).





TITLE: **PWM DAC Board**

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